

WEST

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L20 and (hit near4 (rate or ratio or percentage))	28

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L29

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Set Name Query

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*DB=USPT; PLUR=YES; OP=ADJ*L29 L20 and (hit near4 (rate or ratio or percentage))

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L29*DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*L28 L26 and (hit near4 (rate or ratio or percentage))

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L28L27 L26 and (hit near4 (rate or ratio))

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L27L26 L25 and (cache near6 allocat\$4 with (host or processor or CPU or process or thread or (central adj processing adj unit)))

20

L26L25 L24 and l23 and l22

177

L25L24 (((cache or "L2") adj2 control\$4) or ((fetch or decode) adj2 (unit or section))) with (host or processor or CPU or process or (central adj processing adj unit))

2204

L24L23 (multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit))))

32879

L23

WEST**Freeform Search****Database:**

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 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:
Display: **Documents in Display Format:** **Starting with Number**
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DB=USPT; PLUR=YES; OP=ADJ

<u>L3</u>	L1 and (((repartition\$4 or reallocat\$4 or allocat\$4 or distribut\$4 or assign\$4 or redistribut\$4) near6 (cache or partition or block or way)) same (boot\$4 or reboot\$4 or power\$4 or (turn\$3 on)))	26	<u>L3</u>
<u>L2</u>	L1 and ((repartition\$4 or reallocat\$4) with (boot\$4 or reboot\$4 or power\$4 or (turn\$3 on)))	0	<u>L2</u>
<u>L1</u>	cache and ((operating adj2 system) with (execut\$4 or run\$4) with (partition or section or portion))	457	<u>L1</u>

END OF SEARCH HISTORY

<u>L22</u>	(multiple or several or plurality or second or another) adj2 (cache or "L2")	3743	<u>L22</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L21</u>	L20 and (hit near4 (rate or ratio))	23	<u>L21</u>
<u>L20</u>	L18 and (cache near6 allocat\$4 with (host or processor or CPU or process or thread or (central adj processing adj unit)))	89	<u>L20</u>
<u>L19</u>	L18 and (cache near6 allocat\$4 with (host or processor or CPU or process or (central adj processing adj unit)))	89	<u>L19</u>
<u>L18</u>	L17 and l14	194	<u>L18</u>
<u>L17</u>	((cache or "L2") adj2 control\$4) or ((fetch or decode) adj2 (unit or section))) with (host or processor or CPU or process or (central adj processing adj unit))	4243	<u>L17</u>
<u>L16</u>	((cache or "L2") adj2 control\$4) or ((fetch or decode) adj2 (unit or section)))	9701	<u>L16</u>
<u>L15</u>	L14 and (((cache or "L2") adj2 control\$4) or ((fetch or decode) adj2 (unit or section)))	258	<u>L15</u>
<u>L14</u>	L13 and l1	349	<u>L14</u>
<u>L13</u>	(multiple or several or plurality or second or another) adj2 (cache or "L2")	5576	<u>L13</u>
<u>L12</u>	L11 and ((allocat\$4 or free\$3 or available) near6 (block or line location))	20	<u>L12</u>
<u>L11</u>	L9 and (control\$4 with (access\$4 or read\$4 or writ\$4 or store or load\$4 or fetch\$4) with (block or line location))	68	<u>L11</u>
<u>L10</u>	L9 and (control\$4 near6 (access\$4 or read\$4 or writ\$4 or store or load\$4 or fetch\$4) near8 ((allocable or free\$3 or available) near4 (block or line location)))	0	<u>L10</u>
<u>L9</u>	L2 and ((multiple or several or plurality or second or another) adj2 ((cache or "L2") adj3 control\$4))	203	<u>L9</u>
<u>L8</u>	L2 and ((multiple or several or plurality or second) adj2 ((cache or "L2") adj3 control\$4))	203	<u>L8</u>
<u>L7</u>	L2 and ((multiple or several or plurality or second) adj2 (cache adj3 control\$4))	202	<u>L7</u>
<u>L6</u>	L5 and ((cache or block) near4 allocat\$4)	53	<u>L6</u>
<u>L5</u>	L4 and ((host or processor or CPU or (central adj processing adj unit)) with (cache adj2 control\$4))	78	<u>L5</u>
<u>L4</u>	L3 and ((monitor\$4 or check\$4 or determin\$4 or record\$4 or control\$4 or observ\$4 or supervis\$4 or track\$4 or watch\$4) near6 (cache near4 (hit or performance)))	110	<u>L4</u>
<u>L3</u>	L2 and L1	332	<u>L3</u>
<u>L2</u>	(multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit))))	34433	<u>L2</u>
<u>L1</u>	(cache near3 (partition\$4 or block or line or section\$4 or area or portion\$4 or divi\$4)) near8 (allocat\$4 or distributed or appropriat\$4 or designat\$4 or select\$4) near8 (processor or CPU or host or process)	549	<u>L1</u>

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DB=USPT; PLUR=YES; OP=ADJ

<u>L10</u>	L8 and ((partition\$4 or section\$4 or portion\$4 or divi\$4 or part\$4) near8 (cache or block))	50	<u>L10</u>
<u>L9</u>	L8 and ((partition\$4 or section\$4 or portion\$4 or divi\$4 or part\$4) near8 cache)	50	<u>L9</u>
<u>L8</u>	L7 and (cache near8 (performance or rate or ratio or measur\$4))	51	<u>L8</u>
<u>L7</u>	L6 and ((control\$4 or manag\$4 or arbit\$6) near6 ("12" or second or secondary or lower) adj2 cache))	65	<u>L7</u>
<u>L6</u>	L5 and (((allocat\$4 or assign\$4 or appropriat\$4 or designat\$3) near4 (block or cache or location or line)) with (host or processor or CPU or (central adj processing adj unit)))	184	<u>L6</u>
<u>L5</u>	L4 and ((control\$4 or manag\$4 or arbit\$6) with (interconnect\$3 or link\$4 or bus or channel))	348	<u>L5</u>
<u>L4</u>	L3 and l2 and l1	382	<u>L4</u>
<u>L3</u>	((allocat\$4 or assign\$4 or appropriat\$4 or designat\$3) near4 (block or cache or location or line)) with (cache near4 (control\$4 or manag\$4 or arbit\$6))	993	<u>L3</u>
<u>L2</u>	(host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control\$4 or manag\$4 or arbit\$6))	4915	<u>L2</u>
<u>L1</u>	multi-processor or ((multiple or several or plurality) near4 (processor or CPU or (central adj processing adj unit)))	40798	<u>L1</u>

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L14</u>	l13 and (tag near4 (memory or storage or RAM))	26	<u>L14</u>
<u>L13</u>	L12 and (("L2" or upper or second or secondary) near3 cache)	62	<u>L13</u>
<u>L12</u>	L11 and (cache near8 (hit or miss or ratio or rate or performance or monitor\$4))	74	<u>L12</u>
<u>L11</u>	L10 and ((control\$4 or manag\$4 or arbit\$4) with (interconnect\$3 or link\$4 or bus or channel))	76	<u>L11</u>
<u>L10</u>	L9 and ((cache or system) near8 (part\$4 or partition\$4 or divi\$4 or portion\$4 or section\$4 or repartition\$4 or arrang\$4))	81	<u>L10</u>
<u>L9</u>	L8 and l7 and l6 and l1	87	<u>L9</u>
<u>L8</u>	block with (allocat\$4 or assign\$4 or appropriat\$4 or designat\$4) with (process or processor or CPU or host)	8596	<u>L8</u>
<u>L7</u>	(host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control\$4 or manag\$4 or arbit\$6))	4915	<u>L7</u>
<u>L6</u>	multi-processor or multiprocessor or ((multiple or several or plurality or second) adj2 (host or processor or CPU or (central adj processing adj unit)))	34433	<u>L6</u>
<u>L5</u>	L4 and (((("L2" or upper or second or secondary) near3 cache) near4 control\$4)	26	<u>L5</u>
<u>L4</u>	L3 and (block with (allocat\$4 or assign\$4 or appropriat\$4 or designat\$4) with (process or processor or CPU or host))	67	<u>L4</u>
<u>L3</u>	L2 and ((cache or system or block) near8 (partition\$4 or section\$4 or portion\$4 or divi\$4 or repartition\$4 or part\$4))	384	<u>L3</u>
<u>L2</u>	L1 and ((block or line) with (interconnect\$3 or link\$4 or bus or channel) with control\$4) and (((host or processor or CPU or (central adj processing adj unit)) with (cache near4 (control\$4 or manag\$4 or arbit\$6)))))	426	<u>L2</u>
<u>L1</u>	((711/122 711/118 711/119 711/120 711/129 711/133 711/143)!.CCLS.)	2335	<u>L1</u>

END OF SEARCH HISTORY